



Freeform Search

Database:	US Pre-Grant Publication Full-Text Database
	US Patents Full-Text Database
	US OCR Full-Text Database
	EPO Abstracts Database
	JPO Abstracts Database
	Derwent World Patents Index
	IBM Technical Disclosure Bulletins

Term:	12 same 13 same 14 same 16	 
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Display:	<input type="text" value="10"/>	Documents in Display Format:	<input type="text" value="-"/>	Starting with Number	<input type="text" value="1"/>
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Generate: ☐ Hit List ☒ Hit Count ☐ Side by Side ☐ Image

Search

Clear

Interrupt

Search History

DATE: Saturday, February 18, 2006 [Printable Copy](#) [Create Case](#)

<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
	<i>DB=PGPB,USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>		
<u>L48</u>	12 same 13 same 14 same 16 same shared	0	<u>L48</u>
<u>L47</u>	12 same 13 same 14 same 16	18	<u>L47</u>
<u>L46</u>	bit same (enable or enabling) same (disable or disabling) same 112	2	<u>L46</u>
<u>L45</u>	bit same ((turn or turning) adj2 on) same ((turn or turning) adj2 off)	2	<u>L45</u>
<u>L44</u>	bit same (turn adj2 on) same (turn adj2 off)	1	<u>L44</u>
<u>L43</u>	L42 same 16	66	<u>L43</u>
<u>L42</u>	14 same 138 same memory same shared	9706	<u>L42</u>
<u>L41</u>	L39 same 16	4	<u>L41</u>
<u>L40</u>	L39 same 16	4	<u>L40</u>
<u>L39</u>	14 same 138 same memory same 112	875	<u>L39</u>
<u>L38</u>	(peripheral or (I adj O) or (input adj output)) or 13	3232819	<u>L38</u>
<u>L37</u>	(peripheral or (I adj O) or (input adj output))	1770767	<u>L37</u>
<u>L36</u>	(append or appending or attach or attaching or include or including) same (bit or identifier) same (('end') adj2 (instruction or code)) same (target or destination)	38	<u>L36</u>

<u>L35</u>	l32 and l6	1	<u>L35</u>
<u>L34</u>	l32 and l8	1	<u>L34</u>
<u>L33</u>	l32 and l31	0	<u>L33</u>
<u>L32</u>	('4' adj wire) same ('3' adj wire) same interface (bit or identifier or bits) same (identify or indicate or indicating or determining or determining same identifying) same (target same destination) same instruction	20	<u>L32</u>
<u>L31</u>		132	<u>L31</u>
<u>L30</u>	L29 same l3 same l4 same l6	3	<u>L30</u>
<u>L29</u>	l1 adj2 RAM or NVRAM	10017	<u>L29</u>
<u>L28</u>	l26 and l12	1	<u>L28</u>
<u>L27</u>	l11 same l6 same l12	0	<u>L27</u>
<u>L26</u>	l11 same l6	18	<u>L26</u>
<u>L25</u>	l21 same l8	0	<u>L25</u>
<u>L24</u>	l21 and l8	17	<u>L24</u>
<u>L23</u>	l21 and l12 and l8	5	<u>L23</u>
<u>L22</u>	l21 and l16	0	<u>L22</u>
<u>L21</u>	('no') adj2 (address bus)	83	<u>L21</u>
<u>L20</u>	l16 and l5	59	<u>L20</u>
<u>L19</u>	l16 and l5	59	<u>L19</u>
<u>L18</u>	l16 same l5	0	<u>L18</u>
<u>L17</u>	L16 same l12	0	<u>L17</u>
<u>L16</u>	instruction adj4 identifier	2591	<u>L16</u>
<u>L15</u>	instruction adj4 identifier	5	<u>L15</u>
<u>L14</u>	l8 same l12	5	<u>L14</u>
<u>L13</u>	L12 same l11 same l8	0	<u>L13</u>
<u>L12</u>	shared adj2 bus	5737	<u>L12</u>
<u>L11</u>	l2 same l3 same l4	6683	<u>L11</u>
<u>L10</u>	l8 same l5	26	<u>L10</u>
<u>L9</u>	L8 and l5	884	<u>L9</u>
<u>L8</u>	(bit or identifier) same ((instruction) adj4 (execute or executing or executed))	12130	<u>L8</u>
<u>L7</u>	l6 same l5 same l2 same l4 same l3	0	<u>L7</u>
<u>L6</u>	chip adj2 select	14179	<u>L6</u>
<u>L5</u>	('no') adj2 (address or address bus)	11861	<u>L5</u>
<u>L4</u>	(host or cpu or processor)	1557989	<u>L4</u>
<u>L3</u>	controller or (control adj unit)	1784512	<u>L3</u>
<u>L2</u>	L1 adj2 (memory or RAM)	99488	<u>L2</u>
<u>L1</u>	non adj volatile	157040	<u>L1</u>

END OF SEARCH HISTORY